

## QSFP28-100GB-ER4L-4WDMI-CN2-C

Ciena® XCVR-Q40V31 Compatible TAA 100GBase-ER4L QSFP28 Transceiver (SMF, 1310nm, LC, DOM, 40km, -40 to 85C)

### Features:

- SFF-8665 Compliance
- Duplex LC Connector
- Single-mode Fiber
- Industrial Temperature -40 to 85 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



### Applications:

- 100GBase Ethernet
- Access and Enterprise

### Product Description

This Ciena® XCVR-Q40V31 compatible QSFP28 transceiver provides 100GBase-ER4L throughput up to 40km over single-mode fiber (SMF) using a wavelength of 1295nm to 1309nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Ciena® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

ProLabs' transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



## Regulatory Compliance

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC: compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety: compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

## Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Storage Temperature	T <sub>stg</sub>	-40		85	°C
Operating Case Temperature	T <sub>c</sub>	-40	25	85	°C
Power Supply Voltage	V <sub>cc</sub>	-0.5		4.0	V
Relative Humidity	RH	5		95	%
Data Rate Per Channel			25.78125		Gbps

## Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Supply Voltage	V <sub>cc</sub>	3.135	3.3	3.465	V	
Module Supply Current	I <sub>cc</sub>			1350	mA	
Power Dissipation	P <sub>Diss</sub>			4500	mW	
<b>Transmitter</b>						
Single-Ended Input Voltage Tolerance		-0.3		4.0	V	
Input Differential Impedance	Z <sub>IN</sub>		100		Ω	
Differential Data Input Swing	V <sub>IN,pp</sub>	190		700	mVp-p	
AC Common-Mode Input Voltage Tolerance		15			mVp-p	
Differential Input Voltage Swing Threshold			50		mVp-p	
<b>Receiver</b>						
Single-Ended Output Voltage		-0.3		4.0	V	
Output Differential Impedance	Z <sub>OUT</sub>	90	100	110	Ω	
Differential Data Output Swing	V <sub>OUT,pp</sub>	300		850	mVp-p	
AC Common-Mode Output Voltage				7.5	mVp-p	

## Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
<b>Transmitter</b>						
<b>Launch Optical Power Per Lane</b>	Po	-2.9		+2.9	dBm	1
<b>Total Launch Optical Power</b>	Po			+8.9	dBm	1
<b>Center Wavelength Range</b>	L1	1294.53	1295.56	1296.59	nm	
	L2	1299.02	1300.05	1301.09	nm	
	L3	1303.54	1304.58	1305.63	nm	
	L4	1308.09	1309.14	1310.19	nm	
<b>Extinction Ratio</b>	ER	8.0			dB	2
<b>Spectral Width (-20dB)</b>	$\Delta\lambda$			1	nm	
<b>Side-Mode Suppression Ratio</b>	SMSR	30			dB	
<b>Optical Return Loss Tolerance</b>	ORLT			20	dB	
<b>Pout @Tx_Disable Asserted</b>	Poff			-30	dBm	1
<b>Eye Mask: (X1, X2, X3, Y1, Y2, Y3)</b>		(0.25, 0.4, 0.45, 0.25, 0.28, 0.4)				
<b>Receiver</b>						
<b>Center Wavelength</b>	L1	1294.53	1295.56	1296.59	nm	
	L2	1299.02	1300.05	1301.09	nm	
	L3	1303.54	1304.58	1305.63	nm	
	L4	1308.09	1309.14	1310.19	nm	
<b>Sensitivity Per Channel (OMA)</b>	S			-16.6	dBm	3
	S			-20.5	dBm	4
<b>Overload Per Channel</b>	$P_{max}$	-4.9			dBm	3
<b>Damage Threshold Per Channel</b>	THd	-3.5			dBm	
<b>Receiver Reflectance</b>	RL			-26	dB	
<b>LOS De-Assert</b>	LOSD			-21.0	dBm	
<b>LOS Assert</b>	LOSA	-26.0			dBm	
<b>LOS Hysteresis</b>	LOSH	0.5			dB	

### Notes:

1. The optical power is launched into a single-mode fiber.
2. Measured with a PRBS  $2^{31}-1$  test pattern @25.78125Gbps.
3. Without FEC, measured with PRBS  $2^{31}-1$  test pattern, 25.78125Gbps, and BER  $1.0E^{-12}$ .
4. With FEC, measured with PRBS  $2^{31}-1$  test pattern, 25.78125Gb/s, and BER  $5.0E^{-5}$ .

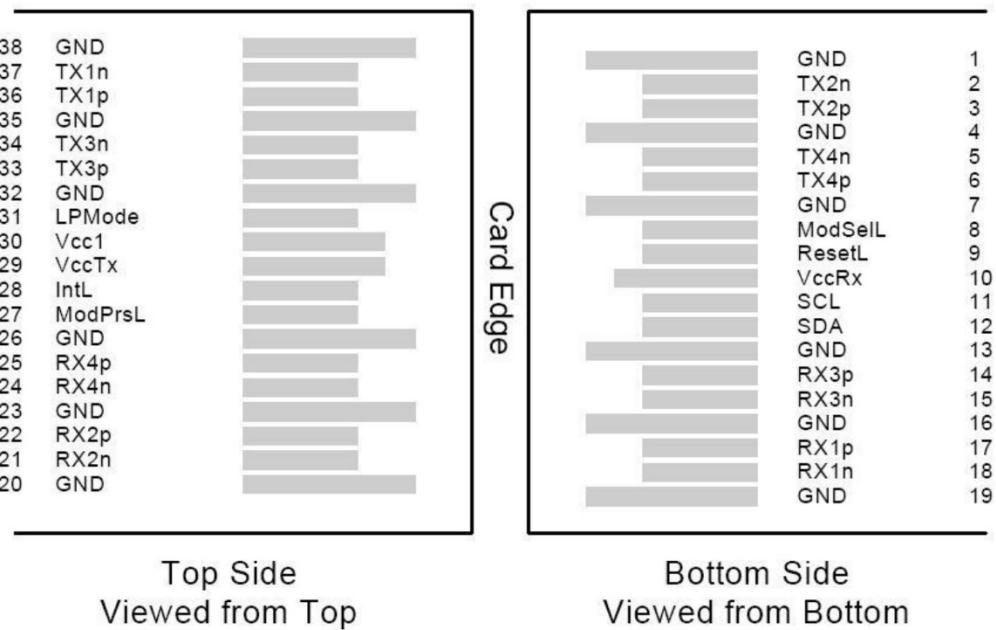
## Pin Descriptions

Pin	Logic	Symbol	Name/Descriptions	Ref.
<b>1</b>		GND	Module Ground.	1
<b>2</b>	CML-I	Tx2-	Transmitter Inverted Data Input.	
<b>3</b>	CML-I	Tx2+	Transmitter Non-Inverted Data Input.	
<b>4</b>		GND	Module Ground.	1
<b>5</b>	CML-I	Tx4-	Transmitter Inverted Data Input.	
<b>6</b>	CML-I	Tx4+	Transmitter Non-Inverted Data Input.	
<b>7</b>		GND	Module Ground.	1
<b>8</b>	LVTTI-I	MODSEIL	Module Select.	2
<b>9</b>	LVTTI-I	ResetL	Module Reset.	2
<b>10</b>		VccRx	+3.3V Receiver Power Supply.	
<b>11</b>	LVCMOS-I	SCL	2-Wire Serial Interface Clock.	2
<b>12</b>	LVCMOS-I/O	SDA	2-Wire Serial Interface Data.	2
<b>13</b>		GND	Module Ground.	1
<b>14</b>	CML-O	Rx3+	Receiver Non-Inverted Data Output.	
<b>15</b>	CML-O	Rx3-	Receiver Inverted Data Output.	
<b>16</b>		GND	Module Ground.	1
<b>17</b>	CML-O	Rx1+	Receiver Non-Inverted Data Output.	
<b>18</b>	CML-O	Rx1-	Receiver Inverted Data Output.	
<b>19</b>		GND	Module Ground.	1
<b>20</b>		GND	Module Ground.	1
<b>21</b>	CML-O	Rx2-	Receiver Inverted Data Output.	
<b>22</b>	CML-O	Rx2+	Receiver Non-Inverted Data Output.	
<b>23</b>		GND	Module Ground.	1
<b>24</b>	CML-O	Rx4-	Receiver Inverted Data Output.	
<b>25</b>	CML-O	Rx4+	Receiver Non-Inverted Data Output.	
<b>26</b>		GND	Module Ground.	1
<b>27</b>	LVTTI-O	ModPrsL	Module Present. Internally pulled down to GND.	
<b>28</b>	LVTTI-O	IntL	Interrupt output. Should be pulled up on the host board.	2
<b>29</b>		VccTx	+3.3V Transmitter Power Supply.	
<b>30</b>		Vcc1	+3.3V Power Supply.	
<b>31</b>	LVTTI-I	LPMode	Low-Power Mode.	2
<b>32</b>		GND	Module Ground.	1
<b>33</b>	CML-I	Tx3+	Transmitter Non-Inverted Data Input.	
<b>34</b>	CML-I	Tx3-	Transmitter Inverted Data Input.	
<b>35</b>		GND	Module Ground.	1
<b>36</b>	CML-I	Tx1+	Transmitter Non-Inverted Data Input.	
<b>37</b>	CML-I	Tx1-	Transmitter Inverted Data Input.	
<b>38</b>		GND	Module Ground.	1

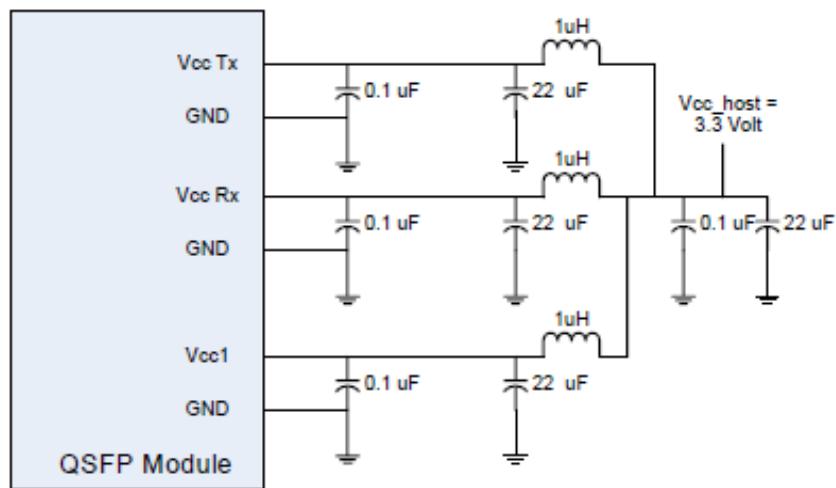
**Notes:**

1. Module circuit ground is isolated from the module chassis ground within the module.
2. Open collector. Should be pulled up with  $4.7\text{k}\Omega$ - $10\text{k}\Omega$  on the host board to a voltage between 3.15V and 3.6V.

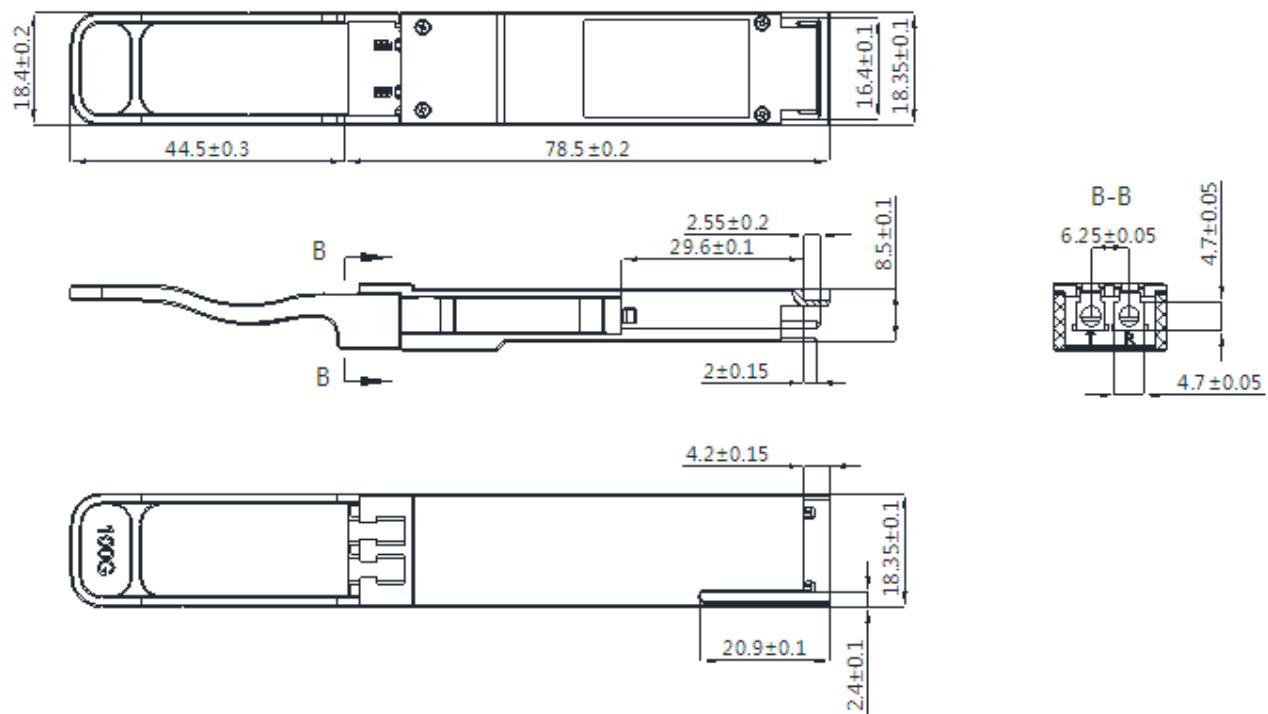
**Electrical Pin-Out Details**



**Recommended Host Board Power Supply Filter Network**



## Mechanical Specifications



## About ProLabs

Our experience comes as standard; for over 15 years ProLabs has delivered optical connectivity solutions that give our customers freedom and choice through our ability to provide seamless interoperability. At the heart of our company is the ability to provide state-of-the-art optical transport and connectivity solutions that are compatible with over 90 optical switching and transport platforms.

## Complete Portfolio of Network Solutions

ProLabs is focused on innovations in optical transport and connectivity. The combination of our knowledge of optics and networking equipment enables ProLabs to be your single source for optical transport and connectivity solutions from 100Mb to 400G while providing innovative solutions that increase network efficiency. We provide the optical connectivity expertise that is compatible with and enhances your switching and transport equipment.

## Trusted Partner

Customer service is our number one value. ProLabs has invested in people, labs and manufacturing capacity to ensure that you get immediate answers to your questions and compatible product when needed. With Engineering and Manufacturing offices in the U.K. and U.S. augmented by field offices throughout the U.S., U.K. and Asia, ProLabs is able to be our customers best advocate 24 hours a day.



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